IN THE CLAIMS

1 (Original). A method of organizing stored information on a non-volatile, re-programmable semiconductor memory comprising:

partitioning said memory into a plurality of partitions, each having a defined address; and

storing the defined address for one partition in another partition.

2 (Original). The method of claim 1 further including storing information about the number of partitions.

3 (Original). The method of claim 1 further including storing a boot loader in one of said partitions.

4 (Original). The method of claim 1 further including storing a file system in one of said partitions.

5 (Original). The method of claim 1 further including storing a kernel for an operating system in one of said partitions.

6 (Original). The method of claim 1 further including storing information in association with said addresses about whether or not an integrity check needs to be done on the data stored at the associated address.

7 (Original). The method of claim 1 further including storing, in association with the address of a partition, information about the type of information stored in the partition.

8 (Original). The method of claim 7 further including storing information about whether or not the information stored at a given partition is a boot loader, a kernel or a file system.

9 (Original). The method of claim 7 including storing information about the load address for said information in association with said address.

10 (Original). A non-volatile, re-programmable semiconductor memory comprising:

a plurality of addressable partitions, including a partition storing an operating system; and

a storage location storing an address for one of said partitions in association with information about the information stored in said partition.

11 (Original). The memory of claim 10 wherein said memory is a FLASH memory.

12 (Original). The memory of claim 10 wherein one of said partitions stores a basic input/output system.

13 (Original). The memory of claim 10 wherein one of said partitions stores a file system.

14 (Original). The memory of claim 10 wherein one of said partitions stores a kernel for an operating system.

15 (Original). The memory of claim 10 wherein one of said partitions stores a boot loader.

Claims 16-25 (Canceled).

26 (Original). A processor-based system comprising:

a processor;

a volatile memory coupled to said processor; and

a re-programmable, non-volatile semiconductor memory coupled to said processor, said semiconductor memory including a plurality of partitions, one of said partitions storing an operating system and another of said partitions storing the addresses of the other partitions in association with information about what is stored in each of said partitions.

27 (Original). The system of claim 26 wherein said semiconductor memory is a FLASH memory.

28 (Original). The system of claim 26 wherein one of said partitions stores a basic input/output system.

29 (Original). The system of claim 26 wherein one of said partitions stores a file system.

30 (Original). The system of claim 26 wherein one of said partitions stores a boot loader.